AMENDMENTS TO THE ABSTRACT

Please replace the Abstract with the following rewritten Abstract.

The present invention is a technique to perform field operations. A shifter [[to]] shifts an operand. A register stores the shifted operand. A shift post processor processes the shifted operand based on at least a control signal and an offset parameter. The shift post processor has a decoder to decode the offset parameter into a mask field. The mask field has a plurality of mask bits, each of the mask bits defining a bit position of the shifted operand to be operated on.

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